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| TRANSMITTAL OF FORMAL DRAWINGS | | | Docket No. Yee 6 |
| In Re Application Of: Oceager P. Yee AUG 0 9 2004 | | | |
| Serial No. | Filing Date Confirmation | n No. Examir | ner Art Unit |
| 10/748,065 | 12/29/2003 | | |
| Invention: SYSTEM AND METHOD FOR DEBUGGING SYSTEM-ON-CHIPS | | | |
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| | | | |
| Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 | | | |
| Transmitted herewith are: | | | |
| 5 sheets of formal drawing(s) for this application. | | | |
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| Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c). | | | |
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| 2 | ora Signature | Dated: | 8/5/ox |
| Thomas J. Onka Synnestvedt Lec P.O. Box 592 Princeton, NJ 0 609-924-3773 ph 609-924-1811 fa | one : . | are being depo U.S. Postal Ser and addressed | his document and attached formal drawings osited on with the rvice as first class mail under 37 C.F.R. 1.8 to the Commissioner for Patents, P.O. Box to VA 22313-1450. |

Signature of Person Mailing Correspondence

Thomas J. Onka

Typed or Printed Name of Person Mailing Correspondence